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J. J. D. PRO

ISSUE CLASSIFICATION	
Class	Subclass

~~PATENT NUMBER~~

U.S. UTILITY Patent Application

QIPF

PATENT CASE

OLP.E.
SEARCHED 102 O.A. 102

APPLICATION NO. 09/921561	CONT/PRIOR F	CLASS 496 327	SUBCLASS 276	ART UNIT 2072 2876	EXAMINER M. Nguyen
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Kazufumi Komura
Satoru Kawamoto

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Delay circuit, semiconductor integrated circuit device containing a delay circuit and delay method

PTO-2010
1233

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drawg. Figs. Drawg. Print Fig.		CLAIMS ALLOWED Total Claims Print Claim for \$0.61	
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